

L.N. 19 of 2017

**Layout-design (Topography) of Integrated Circuits
(Designation of Qualifying Countries, Territories or
Areas) (Amendment) Regulation 2017**

(Made by the Chief Executive under section 24 of the Layout-design (Topography) of Integrated Circuits Ordinance (Cap. 445) after consultation with the Executive Council)

1. Commencement

This Regulation comes into operation on 1 April 2017.

2. Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation amended

The Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation (Cap. 445 sub. leg. B) is amended as set out in section 3.

3. Schedule amended (qualifying countries, territories or areas)

(1) The Schedule, before item “The Republic of Albania”—

Add

“The Islamic Republic of Afghanistan”.

(2) The Schedule, before item “The Republic of Kenya”—

Add

“The Republic of Kazakhstan”.

(3) The Schedule, before item “The Principality of Liechtenstein”—

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Countries, Territories or Areas) (Amendment) Regulation 2017

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Section 3

B1549

Add

“The Republic of Liberia”.

- (4) The Schedule, before item “The Republic of Sierra Leone”—

Add

“The Republic of Seychelles”.

C. Y. LEUNG
Chief Executive

25 January 2017

Explanatory Note

As a member of the World Trade Organization (*WTO*), Hong Kong is obliged to grant the same level of intellectual property protection for layout-designs (topographies) of integrated circuits to nationals of other WTO member countries, territories or areas as to Hong Kong residents.

2. Afghanistan, Kazakhstan, Liberia and Seychelles have acceded to the World Trade Organization Agreement. This Regulation updates the list of qualifying countries, territories or areas contained in the Layout-design (Topography) of Integrated Circuits (Designation of Qualifying Countries, Territories or Areas) Regulation (Cap. 445 sub. leg. B) to include them.